

Natioanl Load Despatch Centre, New Delhi
Transfer Capability between S1- (S2&S3) for August 2020

Issue Date: 28th April 2020

Issue Time: 1800 Hrs

Revision No. 0

Date	Time Period in IST (hrs)	Total Transfer Capability (TTC)	Reliability Margin	Available Transfer Capability (ATC)	Long Term Access (LTA)/ Medium Term Open Access (MTOA)	Margin Available for Short Term Open Access (STOA)	Changes in TTC w.r.t. Last Revision	Comments
1st August 2020 to 31st August 2020	00 - 24	7500	450	7050	3508	3542		
Limiting Constraints (any one or combination thereof)	i. (n-1) contingency of one circuit of 400 kV Udumalpet-Palakkad will lead to Low Voltage at Palakkad/Trichur & (n-1) contingency of one circuit of 400 kV Tirunelveli-Cochin will lead to Low Voltage at Cochin/Trichur. ii. (n-1) of one line of 400kV Mettur – Karamadai will lead to Low Voltage at 400kV Karamadai SS iii. Low Voltage in Kerala (S3)							
Note-1	S1 comprises Andhra Pradesh, Telangana and Karnataka and Goa(SR); S2 comprises Tamil Nadu and Pondicherry; S3 comprises Kerala							
Note-2	(n-1) contingency of 400/220 ICT at Kozhikode is not considered while assessing TTC because of the radial nature of load in North Kerala and System Protection Shcheme (SPS)							

Natioanl Load Despatch Centre, New Delhi
Transfer Capability for Import of S3 for August 2020

Date	Time Period in IST (hrs)	Total Transfer Capability (TTC)	Reliability Margin	Available Transfer Capability (ATC)	Long Term Access (LTA)/ Medium Term Open Access (MTOA)	Margin Available for Short Term Open Access (STOA)	Changes in TTC w.r.t. Last Revision	Comments
1st August 2020 to 31st August 2020	00-24	3250	90	3160	2757	403		
Limiting Constraint (any one or combination thereof)	i. (n-1) contingency of one circuit of 400 kV Udumalpet-Palakkad will lead to Low Voltage at Palakkad/Trichur							
	ii. (n-1) contingency of one circuit of 400 kV Tirunelveli-Cochin will lead to Low Voltage at Cochin/Trichur.							
	iii. Low Voltage in Kerala (S3)							
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Revision No	Date of Revision	Period of Revision	Reason for Revision	Corridor