

Impact on voltage due to high power HVDC operation at low fault level

Suresh V., Mallick A., Borah Palash Jyoti, Thapa Chitra Bahadur and Kumar Subhash Power System Operation Corporation Ltd. (POSOCO) India

SYNOPSIS

In this paper, emphasis is on operational issue of HVDC station at Biswanath Chariali (BNC). There is an issue of heavy spikes in voltage in NER Grid at the time of tripping of +/- 800 kV HVDC Biswanath Chariali-Alipurduar-Agra pole at high power order due to low fault level of NER Grid and disconnection of AC filter banks at an interval of 1 second. Reactive Power support for Multi-terminal HVDC operation at Biswanath Chariali node is in discrete manner. In case of switching of filter banks, there will be step change in voltage. Reactive power injection at BNC node will be more than required due to the size of shunt capacitors and harmonic filters. In case of load throw or power ramping down condition, it creates transient in voltage. It becomes very difficult for system operator to mitigate this dynamic over-voltage condition in low fault level in NER Grid without synchronous condenser and FACT devices such as SVC / STATCOM. Dynamic over voltage condition is mitigated with shunt reactors and opening of 400 kV lines which are not effective means of control.

KEYWORDS

HVDC, Power order, Low fault level, GR, MR, RVO, RPC, GRTS and MRTB

1. INTRODUCTION

Indian power system is one of the largest bulk power systems in the world and has the fastest growth rate. North-Eastern Regional (NER) Grid is the smallest of the 5 operational Regions of Indian Power system with about 2 percent share of the Peak Demand Met of India. North Eastern Regional Load Despatch Centre (NERLDC) is the Apex body to ensure integrated operation of the NER Grid of India as mandated by the Electricity Act, 2003. NERLDC supervises the activities of seven State control areas and Eleven Inter–State Generating Stations (ISGS) [1].

A multi-terminal HVDC transmission system has more than two converter Stations. A multi-terminal HVDC transmission is more complex than an ordinary point-to-point transmission. In particular, the control system is more elaborate and the telecommunication requirement between the stations becomes larger. There are two possible schemes for Multi Terminal Direct Current (MTDC) systems: Constant Voltage Parallel Scheme and Constant Current Series Scheme. The case presented in this paper is in Constant Voltage Parallel Scheme [2],[3].

1.1. Circuit Diagram

The North Eastern Region (Biswanath Chariali) – Eastern Region (Alipurduar) - Northern Region (Agra) HVDC link is the world's first Multi-Terminal project at +/- 800 kV, linking Rectifier Station at Biswanath Chariali and Alipurduar, with Inverter Station at Agra. Power flow order from BNC & APD to Agra is 3000 MW each and in case of reverse power transfer from Agra to BNC is 1000 MW.



Fig. 1. Representation of Multi-terminal HVDC BNC -APD - Agra Link

The system considered has been represented in Fig. 1. 2 X 3000 MW Multi - Terminal HVDC Stations, enables power transfer between North-Eastern, Eastern and Northern Regions in asynchronous mode. Total MVAR Capacity of AC filter banks at BNC, Alipurduar and Agra are 1983 MVAR, 1992 MVAR and 3630 MVAR respectively.

2. CASE STUDY

Ranganadi Power Station, Pare Power Station and Arunachal Pradesh Power System (except Khupi & Deomali areas) were connected with the rest of NER Grid through 400 kV BNC - Ranganadi 1 & 2 Lines as shown in Fig. 2. 132 kV Gohpur- Nirjuli line was kept opened to control over-loading of 132 kV Nirjuli - Lekhi line. At 09:22 Hrs on 29.08.18, +/- 800 kV BNC – Agra Pole 1 tripped and subsequently, 400 kV BNC – Ranganadi 1 and 2, 400 kV Balipara – BNC 1, 132 kV Ranganadi – Pare 2 and 132 kV Pare – Itanagar lines tripped on Over-voltage.

Due to the Grid Disturbance, total generation loss was around 175 MW (Ranganadi: 120 MW and Pare: 55 MW) and total load loss in Arunachal Pradesh State Power System was around 55 MW in Chimpu and Lekhi area of Arunachal Pradesh.

2.1. Operational Issue

In case of Pole tripping, filter banks will also be tripped simultaneously. However, for Power ramping down as in this paper, delay in filter sub bank disconnection shall be at an interval of 1 second. Reason for delay is to avoid bulk switching at the same time as it can result in severe transient in

voltage due to low fault level of NER Grid. This transient is reflected in AC side which creates oscillation in voltage and current which could further lead to mal-operation of Distance Relays in AC side. Maloperation of Distance Relays during the power swing resulted in 2012 India blackouts [4]. During tripping of Pole with high power order, temporary over-voltage was observed on AC grid. The reason was the reduction in reactive power consumption at the converter terminal without a corresponding reduction in the generated reactive power from the compensation equipment such as filters and the shunt capacitor banks which remained connected to the system for few milli-seconds.



Fig. 2. Representation of affected NER-Region

Different control actions such as increased firing angle operation and filter tripping or Dynamic voltage support compensation scheme could be adopted to limit over voltages in case of Reduced Voltage Operation (RVO) operation [5]. During High Power Order (2000 MW) testing with BNC as rectifier and Agra as inverter, the voltage of 400 kV nodes (Bongaigaon, Balipara, Ranganadi, Biswanath Chariali, Misa and Azara) was around 380 kV because of more demand of reactive power compensation at BNC node. During this period, very low voltage was reported by SLDC Assam in their downstream system. Reference [6] proposed requirement on Reactive Power Control (RPC) is that AC voltage step change (pu) should not exceed specified AC voltage expressed as

$$\Delta V = \frac{Q_{switch}}{SCL_{min} - Q_{total}} \tag{1}$$

Where SCLmin is the minimum short circuit level at AC system (MVA) in which switching takes place, Qswitch is the Reactive power step to be imposed and Qtotal is the total Reactive power connected to the converter including the Reactive power to be switched. In this paper Reactive Power Demand versus Active flow at BNC node for Mono-Polar Auto mode of operation is presented in Fig. 3.

Reference [7] proposed two Bipoles can transfer power through same HVDC line or by separate line with different configuration such as with Dedicated Metallic Return (DMR) or without DMR. Both Bipoles can run with different configuration as per availability of converter, line and requirement of power. The details of protocol received from BNC with master control at Agra, for Ground Return to Metallic Return, are as follows: Changeover from Ground Return (GR) to Metallic Return (MR) can be done at BNC by closing of Ground Return Transfer Switch (GRTS) and opening of Metallic Return Transfer Breaker (MRTB). Reference [6] proposed principle of Ground return to Metallic



return mode as shown in Fig. 4. Changeover from MR to GR at BNC can be done by closing of MRTB and opening of GRTS as shown in Fig 5.

Fig. 3. Reactive Power Demand versus Active Power flow



Fig. 4. Circuit Diagram for Ground return to Metallic Return mode

The changeover from GR to MR and vice versa can be executed from Agra by changeover sequence of Master Control at Agra. On the day of tripping, the changeover sequence was carried out by Agra and it was successful at BNC end but likely due to closing of line earth switch instead of opening it by controller of Agra at Agra end, +/- 800 kV BNC – Agra Pole 1 tripped immediately at Agra on Metallic Return Conductor Ground Fault Protection after transfer from ground return mode to metallic return mode.

Due to power order down command being received at BNC, Pole as well as filter banks was in service. With no dc power flow immediately after tripping of Pole 1 at Agra, the AC bus voltage rose significantly at BNC since the filter sub banks were connected. However, Bus Harmonics Over-Voltage protection operated at BNC and Y block was ordered resulting in tripping of Pole 1 at BNC.

The duration of power order down signal being received at BNC and tripping of pole was around 393 milli-seconds as shown in Fig. 6. However, voltage was stabled in Balipara PMU after 360 milli-seconds.



Fig. 5. Switching Diagram at BNC



Fig. 6. Duration for the tripping of pole

Reactive power controller is required to reduce dynamic Over Voltages. It improves the system performance and brings back the system into stable state quickly. Reactive power of a system is dependent on magnitude of the AC voltage and hence it is used to get the modulation index of the PWM waveform in VSC based HVDC operation. With this control, the HVDC system is made self - sustaining in reactive power supply to the loads [8]. In this case, as NER has low fault level, switching of filter banks may create transient in AC side of NER Grid due to which RPC controller may not perform well. Reductions in power transfers are referred to as runbacks. The runbacks are controlled to a rate of 10 MW per second to minimise the frequency disturbance in the AC system [9].

In case of Bipolar operation of +/- 800 kV HVDC Biswanath Chariali-Alipurduar-Agra pole, maximum ramp rate is 300 MW/min and minimum ramp rate is 1 MW/min. Table 1. shows the Short Circuit Level of NER Grid and Filter Selection Matrix with change in power-order during Bipolar mode of operation as per PSS@E studies. It shows low effective short circuit level of NER Grid at high Power order condition. Fig. 7 represents increasing trend of reactive power demand with increase in Power order.

Sl No.	Pd (MW)	Qd (MVAR)	Qf (MVAR)	Filter Selection Matrix (MVAR)	SCL (MVA)
1	200	82	125	125 (1*125)	6893
2	500	250	250	250 (2*125)	6922
3	1000	604	604	604 (2*160+1*159+1*125)	6893
4	1500	1063	1078	1078(3*160+2*159+1*155+1*125)	6893
5	2000	1612	1663	1663(1*160+2*159+2*155+7*125)	6893
6	2500	2002	1983	1983(3*160+2*159+2*155+7*125)	6893
7	3000	2041	1983	1983(3*160+2*159+2*155+7*125)	7272

Table 1: Short Circuit Level of NER Grid and Filter Selection Matrix



Fig. 7. Power order versus reactive Power demand

3. ANALYSIS OF DISTURBANCE RECORDER (DR) AND EVENT LOGGER (EL) OUTPUT

3.1. Tripping of 400 kV Biswanath Chariali – Ranganadi 1 Line:

Biswanath Chariali: DR output as shown in Fig. 8 indicates over-voltage Stage I start in all the R-Y-B Phases at 09:22:41.437 Hrs. The line tripped at 09:22:41.556 Hrs due to DT received.

Ranganadi: DR output as shown in Fig. 9 shows over-voltage protection trip operated at 09:15:04.104 Hrs (not time synchronized with GPS). As per the present settings of 120% with 100 milli-seconds delay for Stage II protection kept at Ranganadi, over-voltage protection operated at Ranganadi (294 kV in R & B phases ~corresponds to 127% of nominal voltage).

🐝 🗚	A1C1Q35FP12018082918e.dat - 29/08/2018 - 09:22:41.437 - Primary - (Peak Type)						- 0	×
CH 💽		• Title	RMS	InstPeak	Phase	InstVal	RefVal	θ×
1 0		- I-R PH	126.697	-33.435	270.740°	310.005	100.494	3
2	www.www.www.www.	I-Y PH	108.685	59.413	73.771*	-188.006	-206.507	2
3 /		- 1-8 PH	109.051	-28.106	205.324°	-95.799	58.332	3
4 -		IN PH	75.594	124.912	261.038°	21.592	-58.120	1
5		V-R PH	299165.848	-535100.198	252.025°	-39074.147	313013.894	4
6 /		V VY PH	297365.613	347196.366	132.229°	-116809.394	-340136.093	: 4
7		V-B PH	301105.643	335768.948	11.477*	316549.734	5724.506	4
8 -		- VN PH	112806.595	-89135.816	249.595°	160640.582	-21 380.291	5
9	1400 1 1320 1 1400 1 19	LINE M CTN	127.399	-88.792	327.793*	-88.792	54.737	2
38 - 39 - 42 - 43 - 44 - 55 - 55 - 63 - 63 - 70 - 71 -		A TR3PMAIN A TR3P TIE N FUSE FAIL A TIECB GEN A TIECB TRL A TIECB TRL A TIECB TRL N M CB A TO N M C	N N N N N N N N N N N N N N N N N N N	N 09224150 N 09224150 N 09224150 N 09224150 N 09224150 N 09224150 A 09224155 A 09224155 A 09224155 A 09224155 A 09224155 A 09224155 N 09255 N 09255 N 09555 N 09555 N 09555 N 09555 N 095555 N 095555 N 095555 N 095555 N 0955555 N 095	94787 09:2 14787 09:2 12787 09:2 12787 09:2 14787 09:2 14787 09:2 14787 09:2 14787 09:2 14787 09:2 14787 09:2 14787 09:2 14787 09:2 17787 17787 09:2 17787 09:2	241.777787 241.777787 241.912787 241.777787 241.777787 241.777787 241.777787 241.777787	002 002 006 002 002 002 001 001 001 001 001 001 001	•

Fig. 8: DR output of BNC for 400 kV BNC-Ranganadi 1 line

🕷 Wednesday 29 August 2018 09.15.04.000.DAT - 29/08/2018 - 09:15:04.106 - Secondary - (Peak Type)									
		▼ Title	RMS	InstPeak	Phase	InstVal	RefVal	MaxPe ● ×	
	MMMMMM	~ VA	293545.391	340996.900	18.182*	280449.900	-483900.500	<u>410990.50</u>	
2		∿ vb	289881.272	-498989.700	257.397*	61149.300	216764.600	426174.80	
3 WWWWWWWWWWWWWWWWWWWWWWWWWWWW	MMM	v ^a vc	293618.720	330852.900	137.456*	-276107.000	221139.200	439583.90	
4	Market	√ vn	58783.729	31414.700	64.682°	65492.200	-46028.400	509450.70	
		- IA	134.285	284.486	138.344°	-113.242	-30.382	320.392	
•	MAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA	∩ IB	130.002	278.962	11.690*	276.200	-24.858	278.962	
		√ ic	138.176	82.860	254.879*	-248.580	-121.528	287.248	
8	······	~ IN	118.353	91.146	148.524*	-85.622	-174.006	237.532	
	0 ms ' 130 ' 200 ' 390 '		IAIN CB OPEN ABRIER HEALTH IAIN-2 RLY OPTO /R OPTO B PBO OPTO	HY A A	09:15:04	164546 124636 09:15	04.257618 C	01 A 00 00 04 00	
31	<u>à: À</u>	—[]ÄÖ	VER VOLTGE OF	PTD N Î	09:15:04	106310 09:15	04.164546	02 ^{WS.}	
TANGANADI Wed - 29/08/2018 09:15:04.137: [Delta X: 31.644 ms (1.582 cyc @ 50 h]fs: 1203.369 H AS: ON [Delta Y: No Bars									

Fig.9: DR output of Ranganadi for 400 kV BNC-Ranganadi 1 line

3.2. Tripping of 400 kV BNC – Ranganadi 2 Line:

Similar to signature of 400 kV BNC – Ranganadi 1 Line, it is observed that BNC tripped at 09:22:41.672 Hrs due to DT received while Ranganadi tripped at 09:22:40.567 Hrs due to overvoltage protection (297 kV in B phase ~corresponds to 128% of nominal voltage).

3.3. Tripping of 400 kV Balipara – Biswanath Chariali 1 Line:

Biswanath Chariali: As per DR output, line tripped at 09:22:41.591 Hrs due to DT received.

Balipara: As per DR output, over-voltage Stage-I triggered in all the R-Y-B Phases at 09:21:40.384 Hrs which deactivated subsequently. No tripping/ protection tripped was observed from DR output. However, it is suspected that electro-mechanical over-voltage relay may have operated resulting in sending DT signal to BNC.

3.4. Tripping of 132 kV Ranganadi – Pare 2 Line

Pare: As per DR output, any trip triggered at 09:22:40.430 Hrs. No over-voltage protection trip was triggered in DR output. However, the voltage in the R, Y and B phases were 96 kV, 91 kV and 88 kV which corresponds to 126%, 120% and 116% of nominal voltage.

As per the settings given by Pare for 132 kV RHEP – Pare 2 Line, settings for over-voltage Stage II is 115% instantaneous.

Ranganadi: No tripping reported.

3.5. Tripping of 132 kV Pare – Itanagar Line:

Pare: As per DR output, no over-voltage protection trip was triggered. However, the voltage in the R, Y and B phases were 96 kV, 92 kV and 88 kV which corresponds to 126%, 121% and 116% of nominal voltage.

Also, as per the settings given by Pare for 132 kV Pare - Itanagar Line, settings for over voltage Stage II is 115% instantaneous.

No tripping was reported at Itanagar.

3.6. Analysis from Transient Fault Recorder of BNC:

BNC: As per TFR output as shown in Fig. 10, High AC voltage and Low AC and DC current was detected at 09:22:41.417 Hrs. Retard was also triggered at the same time. Y block ordered at 09:22:41.632 Hrs. Thereafter, AC Converter Bus and AC Filter Bus Harmonic over-voltage protection tripped the Converter Transformers and AC Filters leading to reduction of AC Voltage.

#	TFR NEA-S1PCP1A1 1 20180829 09 22_41_417000.DAT - 08/29/18 - 09:22-41.417 - Primary - (Peak Type)									
CH		Title	RMS	InstPeak	Phase	InstVal	RelVal	MaxPeak	MinPeak	• X
-		140.11	210.071	204.250	202 555	110.100	103 507	400.007	500 200	
4		UNC 12	213.3/1	-304.300	232.300	204,497	202,202	400.207	-303.700	KV LM
		UNC 12	221.303	303.000	F1 711*	100.151	100.140	434.340	502.000	IM IN
		UAL_L3	213.887	303.060	01.711	130.151	199.143	443.517	-063.036	KV
	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	INT_LI	2077.605	2524.195	79.242	6.038	/.86/	2/24.640	-2/63.9/7	<u>^</u>
2	X X X X X X X X X X X X X X X X X X X	INY_L2	2102.419	-2/58.268	319.238	2087.646	25/6.533	2688.849	-2843.471	A .
Ľ		IVY_L3	2090.733	-2583.900	198.711	-2590.895	-2552.418	2820.105	-2/02.831	A
14	K X X X X X X X X X X X X X X X X X X X	IVU_L1	2086.335	2561.560	49.398	2149.780	2321.704	26/8.665	-2/84.661	A
8	X X X X X X X X X X X	IVD_L2	2105.632	-2667.365	289.204*	425.560	243./11	2/60.252	-2846.224	A
9		IVD_L3	2086.931	2/80.645	168.739*	-25/5.719	-2566.048	2802.178	-26/2.069	A
10		UDL	717.606	710.438	331.471*	706.709	706.503	753.513	-127.347	ĸv
11		IDN	2659.406	2581.832	206.071*	2595.886	2584.912	2822.670	-64.974	A
12		IOL	2657.363	2562.375	205.586*	2548.606	2544.184	2758.762	-256.288	A
13		ALPHA ORDER	13.840	15.481	76.637*	15.732	15.732	166.252	12.487	deg
14		ALPHA MEAS	15.014	15.776	105.784*	15.824	15.824	209.992	-30.000	deg
15		GAMMA MEAS	138.563	137.632	325.472*	138.000	138.800	209.992	-30.000	deg
16		AMIN CEPREV	0.000	0.000	0.000*	0.000	0.000	0.174	0.000	
17		CPRY	26.042	48.000	7.126*	48.000	48.000	48.000	0.000	•
18		CPRD	26.093	5.998	337.025*	47.998	47.998	47.998	0.000	
19		FREQ	50.125	50.197	N/A	50.230	50.230	52.518	49.695	Hz
20			0.000	0.000	0.000"	0.000	0.000	0.000	0.000	
21		UDN	20.633	-3.167	109.270*	-13.979	-13.979	318.827	-243.275	KV
22		UAC_MAX_HOLD	393.545	395.892	91.431*	395.892	395.892	600.940	0.000	KV
23		KC_FACTOR	50.000	50.000	99.976*	50.000	50.000	50.000	50.000	U
24		RUNBACK_IO_LIM	2615.000	2615.000	333.175'	2615.000	2615.000	2615.000	2615.000	A
25		10_CFC	2577.045	2577.045	214.101*	2577.045	2577.045	2579.194	0.000	A
26		IORD_LIM	2576.951	2576.951	2.526*	2576.951	2576.951	2577.031	187.431	A
27		TCP	31.000	31.000	53.161*	31.000	31.000	31.000	31.000	
28		POLE_DC_PWR	1975.828	1992.993	226.739*	1861.729	1861.729	2026.047	-9.977	MW
										Ŧ
1		A BLOCK		A N	09.22-41.6	351700 351700		001		•
3		A BPPO		A A	00.22.41.4	511/00		000		
5		AYBLOCK		ÂÂ	09.22.41.6	31500 09.2	22:41.652800	002		
Ž		A S BLOCK		A A				000		
9 10		A INC GAMMA		- A N	09.22:41.6	571900		001		
11		A AMIN FIR IND A ACTIVE		A A	09.22:41.4	134900 09:2	22:43.898100	294		
16		A HAS SS		A A	09/22-41	1700 092	22-41 632000	000		
18 19		A CUR CONT A ALPHA 90		A A A A				000		-
NEA.S										

Fig. 10: TFR output of BNC

## 4. CONCLUSION

There are various solutions for mitigating the Reactive Power and Dynamic over voltage problem such as FACT devices, Synchronous Condenser and forced commutation [2]. Solution for mitigating these issues need to be planned at the earliest otherwise it will create problem when Renewables penetration will be high which may also create severe transient in lean Hydro period.

## 5. ACKNOWLEDGEMENT

The Authors express their gratitude to the Management of POSOCO for granting permission to publish this paper. The authors are also thankful to NERLDC personnel for their support. The views expressed here are that of the authors only and not necessarily that of the Organizations they represent.

## 6. **REFERENCES**

- [1] Singh, T.S.; Mallick, A.; Chakrabarti, R.; Dey, M.; Jacob, J.; "Low Frequency Local mode oscillations in NER Grid, Validation using model based analysis & Mitigation," NASPI, North American Synchro Phasor Initiative, NASPI Work Group Meeting, 22nd – 23rd March, 2017.
- [2] Kundur, P.; "Power System Stability and Control", TATA McGRAW-HILL, Inc., 2010.
- [3] Kimbark, E.W.; "Direct Current Transmission", Volume 1, WILEY-INTERSCIENCE a division of John Wiley & Sons, Inc., 1971.
- [4] PowerGrid, "Report of the Enquiry Committee on grid disturbance in Northern Region on 30th July 2012 and in Northern, Eastern and North-Eastern Region on 31st July 2012," New Delhi, India, Tech. Rep. GRID_ENQ_REP_16_8_12, 2012.
- [5] Nayak, R.N.; Samsal, R. P.; Sehgal, Y. K.; Mukoo, S; "AC/DC Interactions in multi-infeed HVDC scheme: A case study," IEEE Power India Conference, India, pp. 5, 2006.
- [6] https://nptel.ac.in/courses/108104013/28
- [7] Ahmad, F.; Mishra, R; "LCC HVDC Bipoles Configurations and their comparison with same and separate line", India Conference (INDICON), Annual IEEE, pp. 1-6, 2015.
- [8] Manohar, P.; Kelamane, V.; Kaushik, D.;Ahmed, W.; "Improved control for LCC-VSC Hybrid HVDC System," International conference on Circuits, Control and Communications (CCUBE), pp. 1-5, 2013.
- [9] https://www.ea.govt.nz/dmsdocument/3037-appendix-9-hvdc-bipole-operating-policy